# Department of Electronics and Communication Engineering
## Dr Sudhir Chandra Sur Institute of technology and Sports Complex

**Industry Collaborative Workshop on "VLSI Circuit Design using VHDL"**
**Organised by the Department of ECE**
**In association with IIC SurTech and Livid Design Inc.**

**Date and Venue:** 1st November, 2025, DSCSITSC

**No of Students Participated: 31**

**Speaker/Coordinator:** Dr Arindam Sadhu and Dr pradipta Maiti

**External Speaker:** Mr Saurabh Kumar

Department of ECE in association with IIC SurTech and Livid Design Inc. organised a one day workshop on VLSI on 1st November, 2025. The preliminary objective was to involve the students of diploma colleges with modern technologies like design and fabrication and to inspire them for VHDL coding practice and to aware them about the current industry requirement for VLSI.

Professional trainers from Livid Design Inc. was present during the workshop. Students from different diploma colleges were participated in the workshop

**Outcome of the Workshop**:

Students acquired the knowledge on the following domain of IOT,

1. Introduction to Chip Design
2. VHDL Simulation Environment
3. Circuit Analysis
4. Coding Protocols
5. Design Execution

Few glimpses of the event,

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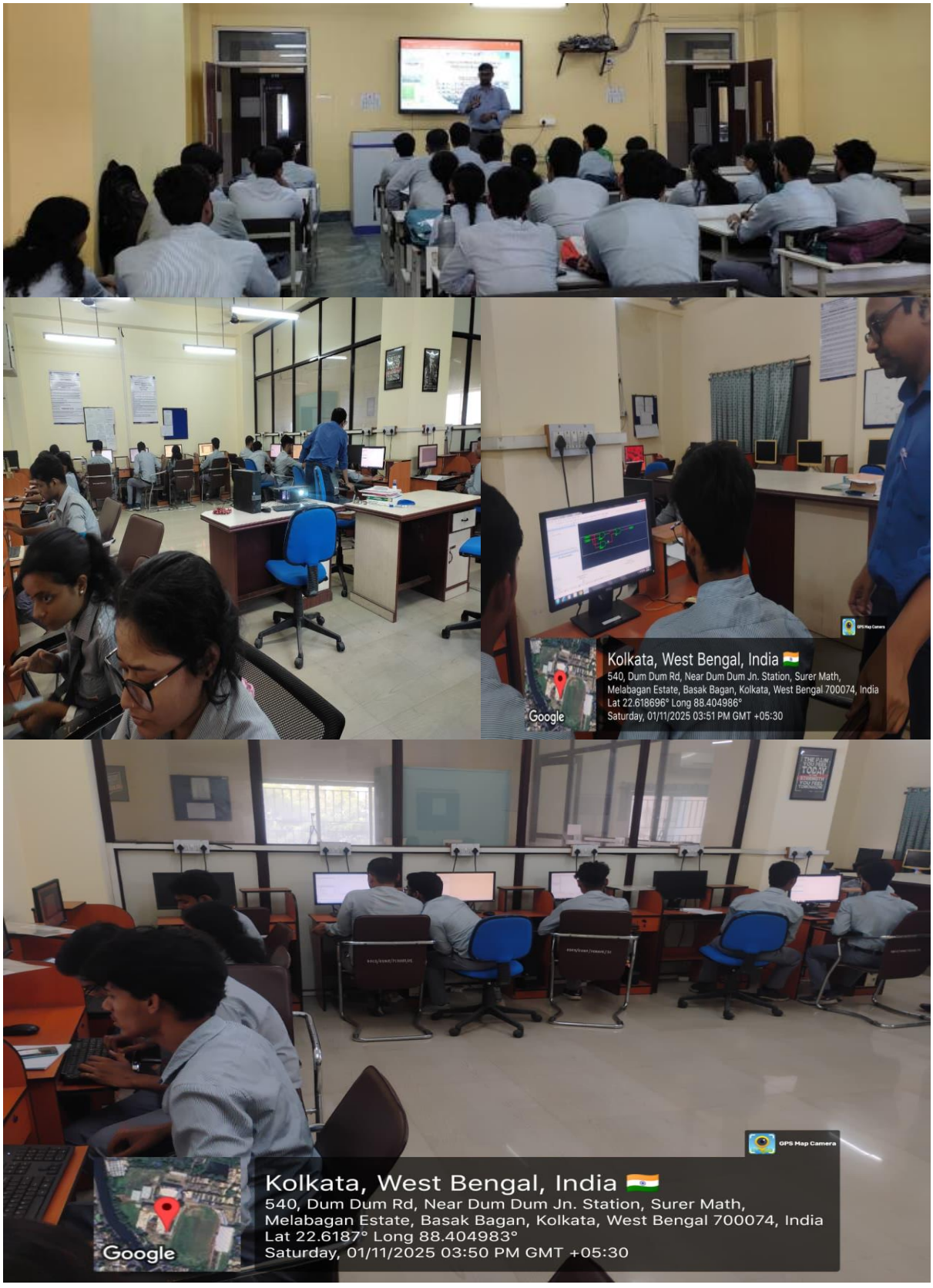


Dr Anirban Neogi

HOD, ECE Department